

TEST MODE SPECIFICATION

BUYER'S MODEL No.	: UBC69XLT,AE69H
UNIDEN No.	: UB334Z/UB334A
VERSION No.	: 1.03
ISSUED DATE	: July 1, 2005
CATEGORY	: SCN
NAME	: Keiji Uetsuki

< UB334Z/UB334A TEST MODE SPECIFICATION >

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NAME	Keiji Uetani

1 LIST OF THE TEST MODE

Following test modes are implemented for this model:

- CH SCAN TEST
- SCN RATE TEST
- VLO SEARCH TEST
- UHF SEARCH TEST
- 12.5K SEARCH TEST
- 10K SEARCH TEST
- SEARCH RATE TEST
- LCD SEG TEST
- SOFT VER TEST
- CHECK SUM TEST
- EEP DMP TEST
- PLL LOCKUP TEST

2 HOW TO ENTER THE TEST MODE

Firstly turn off the unit, then press and hold the corresponding keys for each test mode (see the table below) when you turn on the unit.

Note that an initialization of EEPROM memory must be done before you start using the unit to clear all unnecessary data. Press and hold [2][9][HOLD] keys when you turn on the unit for initialization without BAND PLAN information. Press and hold [3][9][HOLD] keys when you turn on the unit for initialization including BAND PLAN information (All memory clear).

KEYS for TEST	TEST MODE
[2][9][SCAN]	CH SCAN TEST
[1][9][CLR]	PLL LOCKUP TEST
[1][9][SCAN]	SCN RATE TEST
[2][9][FUNC]	VLO SEARCH TEST
[1][9][4]	10K SEARCH TEST
[1][8][SCAN]	12.5K SEARCH TEST
[2][9][1]	UHF SEARCH TEST
[2][9][4]	SEARCH RATE TEST
[2][9][. / CLR]	LCD SEG TEST
[2][9][5]	SOFT VER TEST
[2][9][0]	CHECK SUM TEST
[1][9][HOLD]	EEP DMP TEST

NOTE: In test mode, Key Lock is deactivated and the interval time of the BATTERY LOW Alert is set to 1 second.

3 OPERATION PROCEDURE

3.1 CH SCAN TEST ([2][9][SCAN])

Preset frequencies described below table are programmed in channel memory ("-LoAd-" will be displayed while programming). Afterward scanner starts normal operation in SCAN MODE. Since the channel memories from Ch#13 to Ch#80 are not cleared, you must initialize the memory prior to entering the test mode.

Preset Frequency

CH	Frequency (MHz)	Mode
1	25.0050	FM
2	54.0500	FM
3	86.2750	FM
4	138.1500	FM
5	162.4000	FM
6	173.2200	FM
7	406.8750	FM
8	453.2500	FM
9	511.9125	FM
10	473.35625	FM
11	25.0000	FM
12	512.0000	FM

Additionally, preset search range (25.0000 – 512.0000MHz) will be programmed in Search Bank #1.

3.2 PLL LOCKUP TEST ([1][9][CLR])

Preset frequencies shown in the table in CH SCAN TEST are programmed but Ch#1-10 are locked out. Scanner starts normal operation in SCAN MODE. Additionally, preset search range (25.0000 – 512.0000MHz) will be programmed in Search Bank #1.

3.3 SEARCH TEST

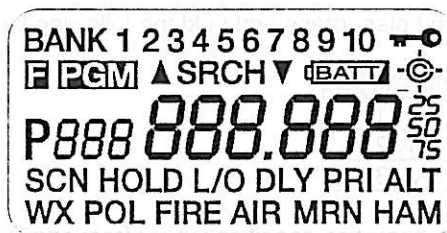
Following search range for each search test described below table are programmed in Search Bank #1. Band plan is automatically set to 1. Afterward, scanner starts upward-search in SEARCH MODE.

TEST MODE	Preset Frequency	Mode/Step
VLO SEARCH TEST ([2][9][FUNC])	28.500 – 28.600 MHz	FM, 5 kHz STEP
UHF SEARCH TEST ([2][9][1])	406.900 – 407.000 MHz	FM, 6.25 kHz STEP
10K SEARCH TEST ([1][9][4])	466.500 – 466.600 MHz	FM, 10 kHz STEP
12.5K SEARCH TEST ([1][8][SCAN])	138.100 – 138.200 MHz	FM, 12.5 kHz STEP

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3.4 LCD TEST ([2][9][./CLR])

All segments/icons are turned on. Press any key to quit the test mode and start normal scanner operation.



LCD all segments

3.5 SCAN RATE TEST ([1][9][SCAN])

163.000MHz is programmed in Ch#1. From Ch#2 to 80, a frequency added 20 kHz step to previous channel frequency is programmed in each channel.
(163.020 for Ch#2, 163.040 for Ch#3, ...)

"-LoAd" is displayed while programming. Afterward, scanner starts normal operation in SCAN MODE.

3.6 SEARCH RATE TEST ([2][9][4])

Following search range is programmed in Search Bank#1. Afterward, scanner starts upward-search in SEARCH MODE.

28.500 – 29.500 MHz (FM, 5 kHz STEP)

3.7 CHECK SUM TEST ([2][9][0])

Checksum number is displayed for 3seconds. Afterward, scanner starts normal operation in SCAN MODE.

3.8 SOFTWARE VERSION TEST ([2][9][5])

Model name (UB Number), Software version and Checksum number are displayed for 3 seconds respectively. Afterward, scanner starts normal operation in SCAN MODE.

3.9 EEP DUMP TEST ([1][9][HOLD])

In this mode you can see the memory data of EEPROM. Memory address and data in the address are displayed. Press [▲]/ [▼] key to increase/decrease the address. Press [./CLR] key to jump to next 256byte block.

4 HOW TO END THE TEST MODE

Turn off the unit to quit test mode. Memory data programmed in test mode is retained. Interval time of the BATTERY LOW Alert is set to 15 seconds.

5 BAND PLAN CHANGE

To change the band plan, press and hold the following key when you turn on the unit.

Key	BAND PLAN
[1]	BAND PLAN 1
[2]	BAND PLAN 2
[3]	BAND PLAN 3

Please refer to the operation specification for frequency range covered in each band plan.

Memory initialization by [3][9][HOLD] will reset the band plan to 1.

Memory initialization by [2][9][HOLD] will not clear the band plan information.

6 History of changes

Version	Date	Author	Comment
0.01	2005/5/20	Keiji Uetsuki	Draft version
1.00	2005/5/23	Keiji Uetsuki	Initial Version
1.01	2005/5/31	Keiji Uetsuki	Removed BAND PLAN CHANGE test mode
1.02	2005/6/8	Keiji Uetsuki	Added PLL LOCK and Search test modes
1.03	2005/6/24	Keiji Uetsuki	Modified Freq range for 10K Search test